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Masterthesis within Electrical Engineering: Portability evaluation of softcore processors for new FPGA technology

Reference Code 10343258 SH EN EXT 1

- Site:	Airbus Defence & Space Munich (ex Astrium SL)
- Target Group:	Student
- Work Contract Type / Working Time:	Final-year thesis / Full time
- Start Date / Duration:	09.01.2017 / 6 MONTHS
- Work Experience:	Not specified
- Functional Area:	ENGINEERING / Design & Development
- Education:	Apprentice, Student / Engineering / Electrical Engineering Apprentice, Student / Engineering / Electrotechnics, Hyperfrequency

Airbus Defence and Space is a division of Airbus Group formed by combining the business activities of Cassidian, Astrium and Airbus Military. The new division is Europe's number one defence and space enterprise, the second largest space business worldwide and among the top ten global defence enterprises. It employs some 40,000 employees generating revenues of approximately €14 billion per year.

Airbus Group is a global leader in aeronautics, space and related services. In 2015, the Group - comprising Airbus, Airbus Defence and Space and Airbus Helicopters - generated revenues of € 64.5 billion and employed a workforce of around 136,600.

Our people work with passion and determination to make the world a more connected, safer and smarter place. Taking pride in our work, we draw on each other's expertise and experience to achieve excellence. Our diversity and teamwork culture propel us to accomplish the extraordinary - on the ground, in the sky and in space.

Description of the job

Are you looking for a final year project? Would you like to discover the work of an engineer? Then apply now! We look forward to you joining us at the Design and Development department.

Location: Ottobrunn
Start: 09.01.2017
Duration: 6 months

You will write your final thesis in the Engineering/Design and Development department which is involved in the development of On-board Field Programmable Gate Array (FPGA)/Application Specific Integrated Circuit (ASIC) logic and processor solutions for satellite subsystems and scientific instruments.

In the scope of these projects, in particular for mass memory systems, there is a chance to move from an older SPARC processor to a new generation of processors, such as ARM.

Tasks

Your exciting topic:

- Performing an architecture evaluation of various chosen processors w.r.t. to overall performance by taking into account things like cache, clocks etc.
- Evaluating portability of the softcore processors into the RTG4 FPGA technology from Microsemi
- Performing all the steps to implement the processor into the FPGA (VHDL design, simulation, Synthesis, Place & Route)
- Documentation of all the evaluation, implementation and various analysis

Skills

You offer:

- Enrolled student (m/f) within Electrical Engineering, Electrotechnics or similar field of study
- good knowledge within VHDL,
- Method Experience within Simulation, Place & Route for FPGAs
- Experienced in and being amazed by processor architectures
- English: advanced

Contact

Does this job description fit your objectives and profile? Take the next step in your career and come and join us!

How to apply:

Online via www.jobs.airbusgroup.com

Reference number 10343258

Please provide the following documents: cover letter, C.V., relevant certificates, current certificate of enrolment

You can direct your cover letter to: Mrs. Hansen

Should you have general questions regarding this position you can write an E-Mail to: students.germany@airbus.com

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